

## Overview

iW-SDRAM Controller provides a simplified interface to the industry standard SDR SDRAM memory. It can be configured to support different standard SDR SDRAM chips.

## Features

- ❖ Interface to Single Data Rate SDRAM devices
- ❖ Configurable timing parameters :
  - ❖ CAS latency
  - ❖ tRP
  - ❖ tRCD
  - ❖ tREFC
- ❖ Supports Mode Register configuration
- ❖ Configurable for different SDRAM sizes (64 Mbit / 128 Mbit)
- ❖ No. of banks per device can be configured 2 or 4 banks
- ❖ Sequential Read / Write burst operations with burst length of 1, 2, 4, 8 or Full Page
- ❖ Application controllable commands for SDRAM operations
  - ❖ Normal Operation (Read / Write)
  - ❖ Refresh
  - ❖ Precharge
  - ❖ Load Mode Register
- ❖ Auto Refresh generation support with Programmable Refresh counter
- ❖ Simple DMA Interface for easy integration into any application

## Core Benefits

- ❖ Eases SOC integration
- ❖ Configurable for ease of use

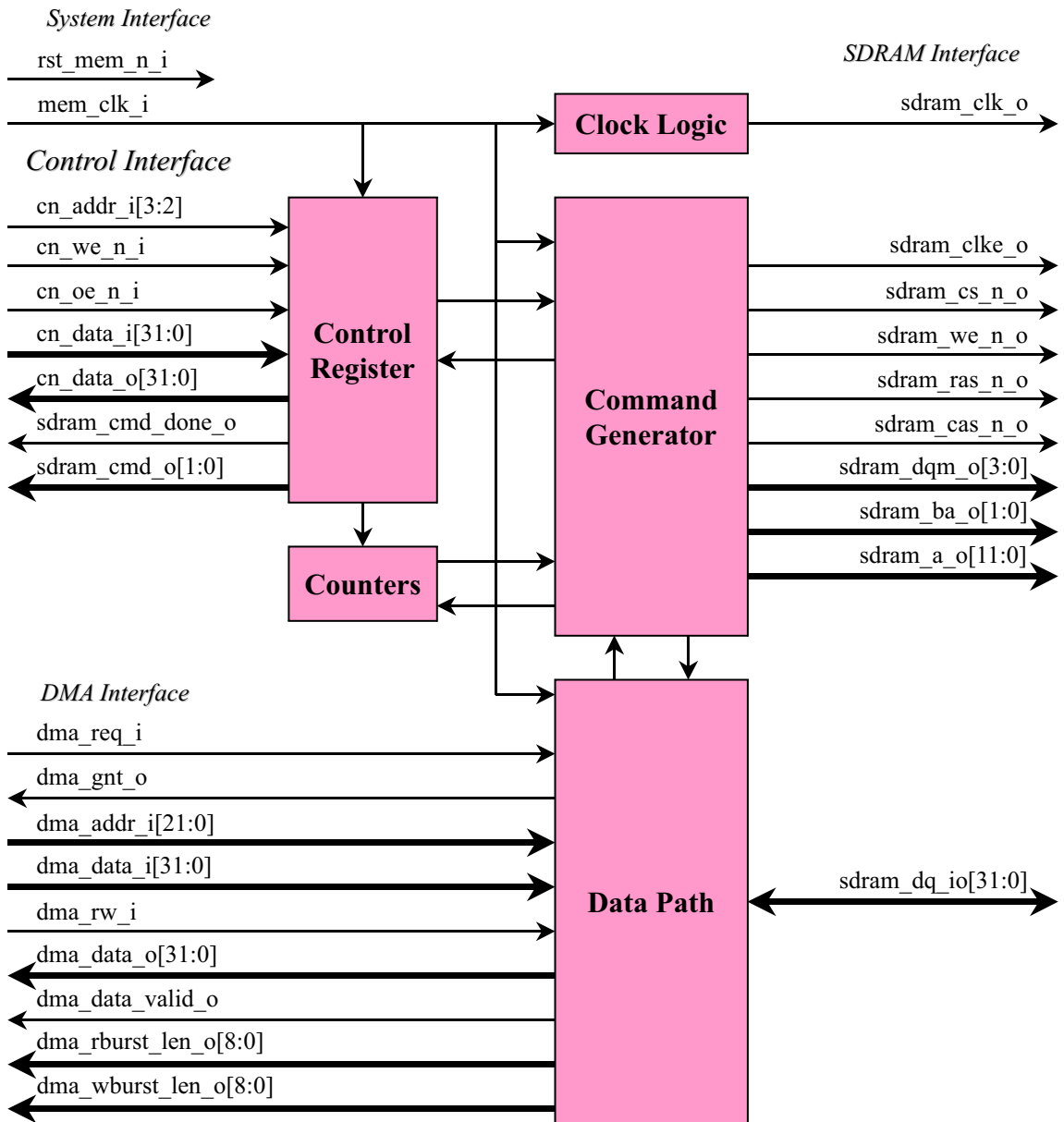
## Core Application

- ❖ Can be used in applications which require high memory throughput, high clock rates and full programmability
- ❖ Can be used in applications which use Single Data Rate SDRAM devices

## Deliverables

- ❖ Technical Specification
- ❖ RTL Verilog Synthesizable Code
- ❖ Comprehensive Test Environment
- ❖ Technical Support and Maintenance

**Block Diagram**



**About Us**

iWave Systems Technologies is an embedded Hardware and Software Turnkey Design Services focused on providing integrated solutions for developing innovative products and systems in the areas of Communication, Consumer Electronics and Multimedia. iWave offers complete turnkey solutions for systems engineering and product development.

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